We claim:

1. A video output system for producing video signals, the system comprising:

a receiver for receiving a video signal;

a video pipeline for post-processing the received video signal, the video pipeline producing a post-processed video signal; and

a video output module for converting the post-processed video signal, the video output module producing a formatted video signal.

2. The video output system according to claim 1 wherein the video output module further comprises:

an ancillary data injector, the injector inserting ancillary data into the post-processed video signal.

3. The video output system according to claim 1, further comprising:

a generator locking device.

4. The video output system according to claim 1 wherein the video input module includes a generator locking device.

5. The video output system according to claim 1 wherein the received video signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded video signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.

6. The video output system according to claim 1 wherein the received video signal is forwarded from a storage medium.

7. The video output system according to claim 1 wherein the received video signal is forwarded from a video graphics processor.

8. The video output system according to claim 1 wherein the received video signal is forwarded from a video input system.

9. The video output system according to claim 1 wherein the formatted video signal is VS, wherein VS is an analog composite video signal, an analog component video signal, a serial digital composite video signal, a serial digital component video signal, a parallel digital composite video signal, or a parallel digital component video signal.

10. The video output system according to claim 1 wherein the process of post-processing includes region of interest selection.

11. The video output system according to claim 1 wherein the process of post-processing includes frame rate matching.

12. The video output system according to claim 1 wherein the process of post-processing includes spatial adaptation.

13. The video output system according to claim 12 wherein the process of spatial adaptation includes scaling.

14. The video output system according to claim 12 wherein the process of spatial adaptation includes picture framing.

15. The video output system according to claim 14 wherein the process of picture framing includes letter boxing.

16. The video output system according to claim 1 wherein the process of post-processing includes changing the sample rate of the video signal being post-processed.

17. The video output system according to claim 1 wherein the process of post-processing includes gamma removal.

18. The video output system according to claim 1 wherein the process of post-processing includes gamma insertion.

19. The video output system according to claim 1 wherein the process of post-processing includes color space conversion

20. The video output system according to claim 1 wherein the process of post-processing includes changing frames of video data into interleaved fields of video data.

21. The video output system according to claim 1 wherein the process of post-processing includes addressing on a frame-by-frame basis the video signal being post-processed.

22. The video output system according to claim 1 wherein the system is a Peripheral Component Interconnect circuit board.

23. A method for producing video signals, the method comprising:

receiving a video signal;

post-processing the received video signal through a video pipeline, producing a post-processed video signal; and

converting the post-processed video signal, producing a formatted video signal.

24. The method according to claim 23, further comprising:

inserting ancillary data into the post-processed video signal prior to converting the post-processed video signal.

25. The method according to claim 23 , further comprising:

generator locking the received video signal.

26. The method according to claim 23 wherein the video output module includes a generator locking device.

27. The method according to claim 23 wherein the received video signal is e-VS, wherein e-VS is an RGB encoded video signal, an RGBA encoded video signal, a YUV-Type encoded video signal, or a YUVA-Type encoded video signal.

28. The method according to claim 23 wherein the received video signal is forwarded from a storage medium.

29. The method according to claim 23 wherein the received video signal is forwarded from a video graphics processor.

30. The method according to claim 23 wherein the received video signal is forwarded from a video input system.

31. The method according to claim 23 wherein the formatted video signal is VS, wherein VS is an analog composite video signal, an analog component video signal, a serial digital composite video signal, a serial digital component video signal, a parallel digital composite video signal, or a parallel digital component video signal.

32. The method according to claim 23 wherein the process of post-processing includes region of interest selection.

33. The method according to claim 23 wherein the process of post-processing includes frame rate matching.

34. The method according to claim 23 wherein the process of post-processing includes spatial adaptation.

35. The method according to claim 34 wherein the process of spatial adaptation includes scaling.

36. The method according to claim 34 wherein the process of spatial adaptation includes picture framing.

37. The method according to claim 36 wherein the process of picture framing includes letter boxing.

38. The method according to claim 23 wherein the process of post-processing includes changing the sample rate of the video signal being post-processed.

39. The method according to claim 23 wherein the process of post-processing includes gamma removal.

40. The method according to claim 23 wherein the process of post-processing includes gamma insertion.

41. The method according to claim 23 wherein the process of post-processing includes color space conversion.

42. The method according to claim 23 wherein the process of post-processing includes changing frames of video data into interleaved fields of video data.

43. The method according to claim 23 wherein the process of post-processing includes addressing on a frame-by-frame basis the video signal being post-processed.

44. A video output system for producing video signals, the system comprising:

means for receiving a video signal;

means for post-processing the received video signal through a video pipeline, producing a post-processed video signal; and

means for converting the post-processed video signal, producing a formatted video signal.

45. The system according to claim 44, further comprising:

means for inserting ancillary data into the post-processed video signal prior to converting the post-processed video signal.